

2. (AMENDED) The circuit of Claim 1, wherein said programmable functional component includes:

a programmable interconnect for coupling components to said bus;

an analog functional block configurable to perform analog functions, said analog functional block coupled to said interconnect; and

a digital functional block configurable to perform digital functions, said digital functional block coupled to said interconnect.

12. (AMENDED) The electronic device dynamic configuration method of Claim 10 wherein said electronic device is a programmable integrated circuit.

VERSION OF AMENDMENTS WITH CHANGES SHOWN:

IN THE TITLE

Please replace the original title with the following new title:

A SYSTEM AND METHOD OF DYNAMICALLY RECONFIGURING A
PROGRAMMABLE INTEGRATED CIRCUIT [SYSTEM ON A CHIP]

IN THE SPECIFICATION

Please replace the paragraph beginning at page 1, line 1 with the following:

A SYSTEM AND METHOD OF DYNAMICALLY RECONFIGURING A
PROGRAMMABLE INTEGRATED CIRCUIT [SYSTEM ON A CHIP]

Please replace the paragraph beginning at page 1, line 4 with the following: